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INFORMATION DISCLOSURE CITATION IN AN APPLICATION May 20, 2004 (Use several sheets if necessary)		FIRST NAMED INVENTOR Gabriele Manganaro		FILING DATE September 26, 2003
		EXAMINER MNH NORTON	CONFIRMATION NO. 4728	GROUP 2816

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>M</i>	AR	Gray, P. R. and Meyer, R. G., "Analysis and Design of Analog Integrated Circuits, 3rd Ed.," pp. 236-241, John Wiley & Sons, Inc., New York, 1977.
<i>M</i>	AS	Mizuno, M., et al., "A 3-mW 1.0-GHz Silicon-ECL Dual-Modulus Prescaler IC," <i>IEEE Journal of Solid-State Circuits</i> , 27(12): 1794-1798, (1992).
<i>M</i>	AT	A. M. Niknejad, J. L. Tham, and R. G. Meyer, "Fully-Integrated Low Phase Noise Bipolar Differential VCOs at 2.9 and 4.4 GHz," Proceedings of the 25th European Solid-State Circuits Conference, 1999. pp.198-201. http://rfic.eecs.berkeley.edu/~niknejad/publications.html
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EXAMINER <i>Norton</i>	DATE CONSIDERED <i>7/28/04</i>
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